

62
11-20-01
ed.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

NEVILL et al.

Atty. Ref.: 550-241

Serial No. Unknown

Group:

Filed: June 25, 2001

Examiner:

For: **HARDWARE INSTRUCTION TRANSLATION WITHIN A PROCESSOR PIPELINE**

* * * * *

June 25, 2001

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

PRELIMINARY AMENDMENT

In order to place the above-identified application in better condition for examination,
please amend the application as follows:

IN THE CLAIMS

Please cancel claims 23, 26, 27 and 28 without prejudice or disclaimer.

Please substitute the following amended claims for corresponding claims previously
presented. A copy of the amended claims showing current revisions is attached.

3. (Amended) Apparatus as claimed in claim 1, wherein said translator output signals include control signals that control operation of said processor core and match control signals produced on decoding instructions of said first instruction set.

al
cond.